



03/13/97

501.20699VC2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): R. HORI, et al.

U.S. Patent No.: 5,493,572

Issued: February 20, 1996

Serial No.: 07/869,851

Filed: April 16, 1992

For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH
VOLTAGE LIMITER HAVING DIFFERENT OUTPUT
RANGES FROM NORMAL OPERATION AND
PERFORMING OF AGING TEST

Group: 2306

Examiner: S. Baker

APPROVED

JUN 1997

FOR THE COMMISSIONER - PT & TM

CERTIFICATE

MAR 25 1997

OF CORRECTION

PETITION FOR CERTIFICATE OF CORRECTION
UNDER 37 CFR §1.322 OFFICE MISTAKE

Assistant Commissioner for
Patents
Washington, DC 20231

March 13, 1997

Sir:

Applicants hereby petition for a Certificate of Correction for U.S. Patent No. 5,493,572 due to a typographical error with respect to one of the applications listed under the "Foreign Application Priority Data". Particularly, Japanese application 57-220083 was incorrectly listed in the patent as 56-220083. Japanese application 57-220083 was correctly identified in paragraph No. 8 of the April 16, 1992 Request Form for File Wrapper Continuing Application Under 37 CFR §1.62. Accordingly, correction of this matter is respectfully requested as per the attached Certificate of Correction Form.

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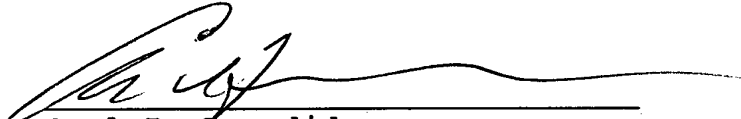
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To the extent necessary, applicants petition for an extension of time under 37 C.F.R. section 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (Case No. 501.20699VC2) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

(703) 312-6600
CIB/hpg



Carl I. Brundidge
Registration No. 29,621
ANTONELLI, TERRY, STOUT & KRAUS, LLP

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 5,493,572
DATED : February 20, 1996
INVENTOR(S) : Ryoichi Hori, Kiyoo Itoh and Hitoshi Tanaka

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page, left-hand column, under the heading
"[30] Foreign Application Priority Data" delete "Dec. 17, 1982
[JP] Japan 56-220083" and insert -- Dec. 17, 1982 [JP]
Japan 57-220083 --

MAILING ADDRESS OF SENDER:

ANTONELLI, TERRY, STOUT & KRAUS
1300 Seventeenth Street
Suite 1800
Arlington, VA 22209

FORM PTO 1050 (REV. 3-82)

PATENT NO. 5,493,572

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